

MCCOG22405A6W-SPTLYI	2 x 24	5mm Character Height	LCD Module
Specification			
Version: 1		Date: 18/09/2013	
Revision			
1	20/10/2011 17/09/2013	First Issue. Modify reliability information	

Display Features			
Character Count	2 x 24		
Appearance	Black on Yellow/Green		
Logic Voltage	3V/5V		
Interface	I2C		
Font Set	English / Japanese		
Display Mode	Transflective		
Character Height	4.67mm		
LC Type	STN		
Module Size	86.20 x 24.70 x 6.00mm		
Operating Temperature	-20°C ~ +70°C		
Construction	COG		
LED Backlight	Yellow/Green	Box Quantity	Weight / Display
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* - For full design functionality, please use this specification in conjunction with the RW1062 specification. (Provided Separately)

Display Accessories	
Part Number	Description
MCCOG-I2C-I-8	Fine pitch(1.27mm) COG I2C interface board. Compatible with both Arduino and UC32 controller boards.

Optional Variants		
Fonts	Appearances	Voltage

General Specification

The Features of the Module is description as follow:

- Module dimension: 86.2 x 24.7 x 6.0(MAX) mm3
- View area: 72.3 x 11.84 mm2
- Active area: 70.3 x 9.84 mm2
- Number of Characters: 24 characters x 2 Lines
- Dot size: 0.45 x 0.54 mm2
- Dot pitch: 0.50x 0.59 mm2
- Character size: 2.45 x 4.67 mm2
- Character pitch: 2.95 x 5.17 mm2
- LCD type: STN Positive, Yellow Green Transflective
- Duty: 1/17DUTY,1/5BIAS
- View direction: 6 o'clock
- Backlight Type: LED, Yellow Green

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Interface Pin Function

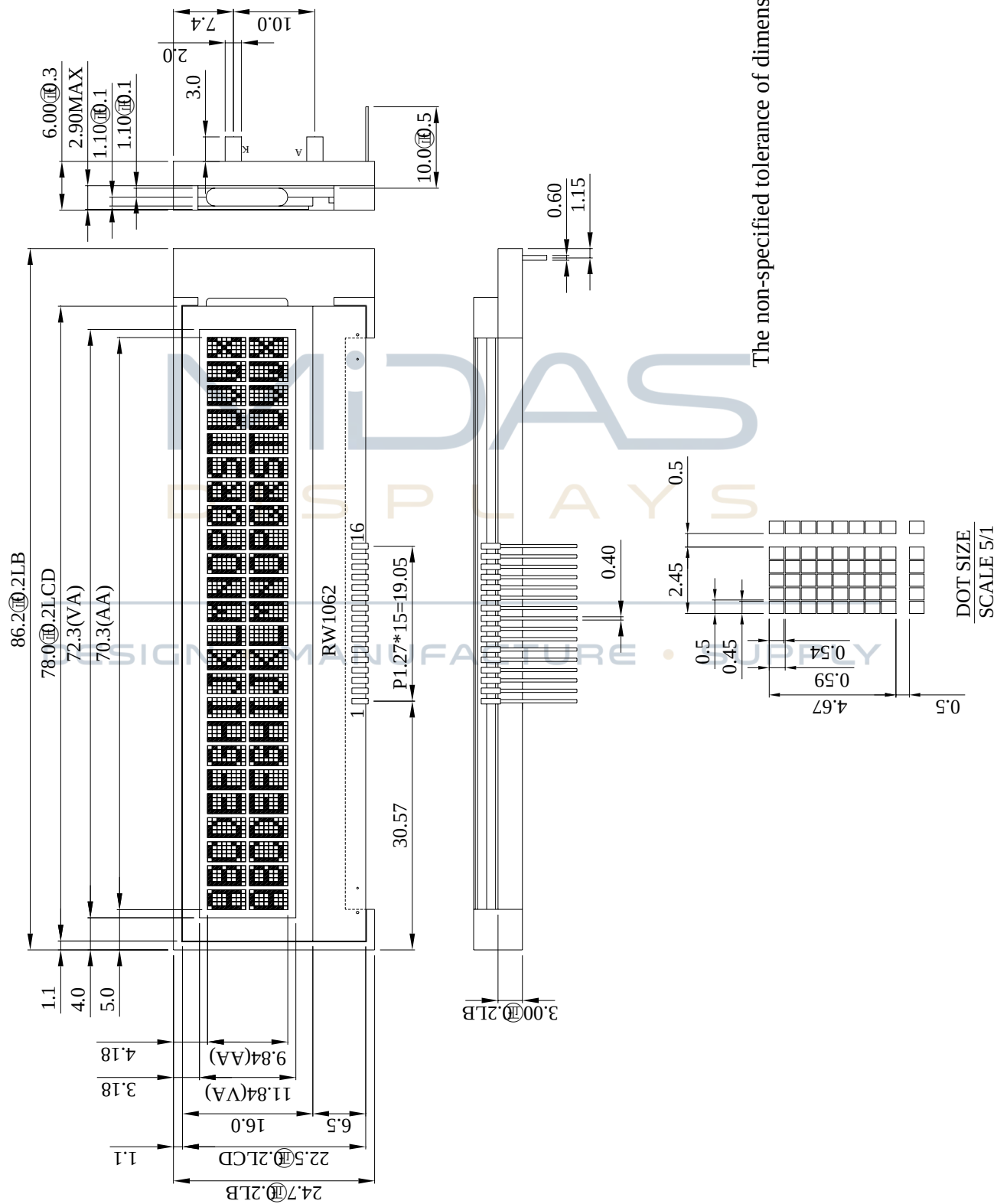
Pin No.	Symbol	Level	Description
1	XRESET		Reset pin, Initialized to Low
2~6	V0~V4		Bias voltage level for LCD driving
7	D6(SID)		Serial input data
8	D7(SCLK)		Serial clock
9	VSS		GND
10	VDD	3.0/5.0	Power supply
11	VOUT		Voltage converter output voltage
12	CAP2N		External Capacitance input, To use the voltage converter (2 times/ 3times), these pins must be connected to the external capacitance.
13	CAP2P		
14	CAP1P		
15	CAP1N		
16	VR		Reference voltage input to generate V0

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Outline Dimension

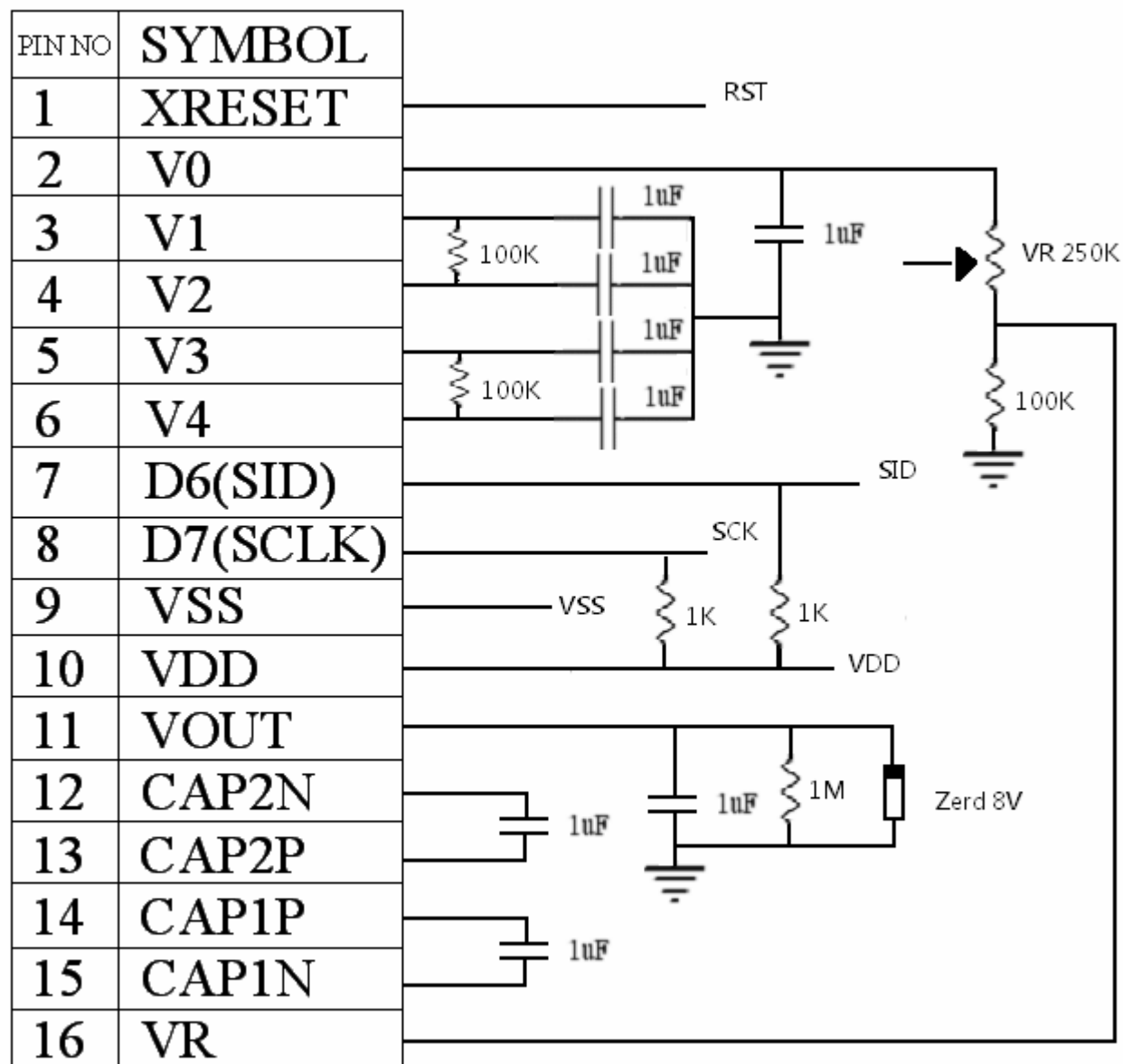
PIN NO	SYMBOL
1	XRESET
2	V0
3	V1
4	V2
5	V3
6	V4
7	D6(SID)
8	D7(SCLK)
9	VSS
10	VDD
11	VOOUT
12	CAP2N
13	CAP2P
14	CAP1P
15	CAP1N
16	VR



The non-specified tolerance of dimension is ±0.2mm.

DOT SIZE
SCALE 5/1

Application schematic



INITIALIZE:

```
MOV      I2C_CONTROL,#00H ;WRITE COMMAND
MOV      I2C_DATA,#30H    ;Function Set RE=0
LCALL    WRITE_CODE
LCALL    DELAY_39uS
MOV      I2C_DATA,#30H    ;Function Set RE=0
LCALL    WRITE_CODE
LCALL    DELAY_39uS
MOV      I2C_DATA,#08H    ;DISPLAY OFF
LCALL    WRITE_CODE
LCALL    DELAY_39uS
MOV      I2C_DATA,#06H    ;Entry Mode Set
LCALL    WRITE_CODE
LCALL    DELAY_39uS
LCALL    CGRAM
MOV      I2C_CONTROL,#00H
MOV      I2C_DATA,#34H    ;Function Set RE=1
LCALL    WRITE_CODE
LCALL    DELAY_39uS
MOV      I2C_DATA,#02H    ;Standby Mode Set: (RE = 1)
LCALL    WRITE_CODE      ;NORMAL MODE
LCALL    DELAY_39uS
MOV      I2C_DATA,#06H    ;Entry Mode Set: (RE = 1)
LCALL    WRITE_CODE      ;SEG NORMAL COM REVERSE
LCALL    DELAY_39uS
MOV      I2C_DATA,#16H    ;Booster on, Regulator on, Follower on
LCALL    WRITE_CODE
LCALL    DELAY_39uS
MOV      I2C_DATA,#08H    ;Extended Function Set (RE = 1)
LCALL    WRITE_CODE
LCALL    DELAY_39uS
MOV      I2C_DATA,#08H    ;Extended Function Set (RE = 1)
LCALL    WRITE_CODE
LCALL    DELAY_39uS
MOV      I2C_CONTROL,#00H
MOV      I2C_DATA,#30H    ;Function Set RE=0
LCALL    WRITE_CODE
LCALL    DELAY_39uS
MOV      I2C_DATA,#0CH    ;DISPLAY ON,Cursor OFF,Cursor Blink OFF
```



```
LCALL WRITE_CODE  
;LCALL DELAY_39uS  
MOV      I2C_DATA,#01H      ;CLEAR DISPLAY  
LCALL WRITE_CODE  
LCALL DELAY_39uS  
LCALL DELAY_39uS  
RET
```



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Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T_{OP}	-20	—	+70	°C
Storage Temperature	T_{ST}	-30	—	+80	°C
Supply voltage for Logic	V_{DD}	-0.3	—	5.5	V
LCD Driver Voltage	V_O	$V_{SS}+0.7$		$V_{SS}-0.3$	V

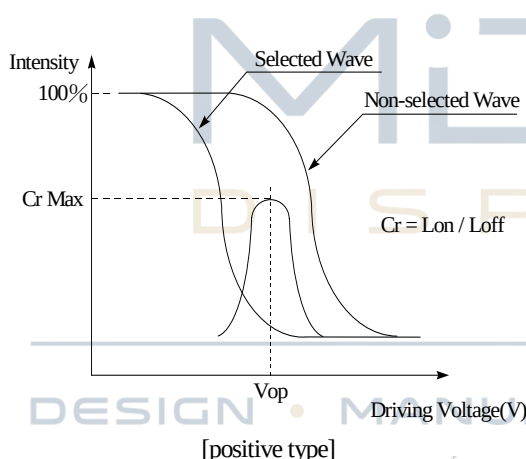
Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	$V_{DD}-V_{SS}$	—	—	3.0/5.0	—	V
Supply Voltage For LCD	V_O-V_{SS}	$T_a=-20^{\circ}\text{C}$	—	—	—	V
		$T_a=25^{\circ}\text{C}$	4.3	4.5	4.7	V
		$T_a=70^{\circ}\text{C}$	—	—	—	V
Input High Volt.	V_{IH}	—	$0.7 V_{DD}$	—	V_{DD}	V
Input Low Volt.	V_{IL}	—	-0.3	—	0.6	V
Output High Volt.	V_{OH}	—	$0.75 V_{DD}$	—	—	V
Output Low Volt.	V_{OL}	—	—	—	$0.2V_{DD}$	V
Supply Current(No include LED Backlight)	I_{DD}	—	—	0.92/ 2.01	—	mA

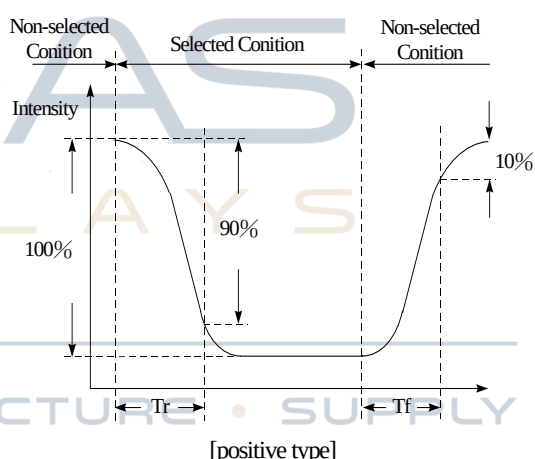
Optical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
View Angle	(V) θ	$CR \geq 2$	20	—	40	deg
	(H) ϕ	$CR \geq 2$	-30	—	30	deg
Contrast Ratio	CR	—	—	3	—	—
Response Time	T rise	—	—	200	300	ms
	T fall	—	—	200	300	ms

Definition of Operation Voltage (Vop)



Definition of Response Time (Tr, Tf)



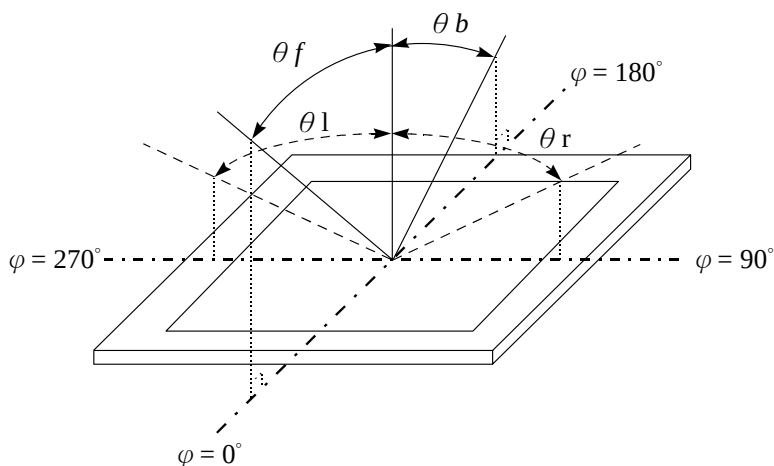
Conditions :

Operating Voltage : Vop

Viewing Angle(θ , ϕ) : 0° , 0°

Frame Frequency : 64 HZ Driving Waveform : 1/N duty, 1/a bias

Definition of viewing angle($CR \geq 2$)



Font table

RW1062 Font table (0A-001)

b7~4 b3~0	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000 CG RAM [00]				0	a	P	^	P				-	9	3	e	P
0001 CG RAM [01]			!	1	A	Q	a	4			=	7	チ	4	8	9
0010 CG RAM [02]			"	2	B	R	b	r			「	イ	ウ	×	P	Θ
0011 CG RAM [03]			#	3	C	S	c	s			」	ウ	テ	て	e	ω
0100 CG RAM [04]			\$	4	D	T	d	t			√	工	ト	ト	H	α
0101 CG RAM [05]			%	5	E	U	e	u			・	ホ	ホ	1	ε	0
0110 CG RAM [06]			&	6	F	V	f	v			ヲ	カ	ニ	ヨ	P	Σ
0111 CG RAM [07]			'	7	G	W	g	w			フ	ホ	ヲ	ヲ	9	π
1000 CG RAM [00]			(	8	H	X	h	x			イ	ウ	ホ	リ	ホ	Σ
1001 CG RAM [01]			)	9	I	Y	i	y			ウ	ウ	ル	ル	リ	Y
1010 CG RAM [02]			*	#	J	Z	j	z			エ	コ	ル	ル	j	チ
1011 CG RAM [03]			+	#	K	L	k	{			ホ	サ	ヒ	ロ	*	万
1100 CG RAM [04]			,	<	L	#	1	1			ホ	エ	フ	フ	ホ	万
1101 CG RAM [05]			-	=	M	I	m	}			ユ	ズ	ル	ル	ト	÷
1110 CG RAM [06]			・	>	N	^	n	+			ヨ	エ	ホ	ホ	ル	
1111 CG RAM [07]			/	?	O	_	o	+			ウ	ウ	マ	マ	ル	■

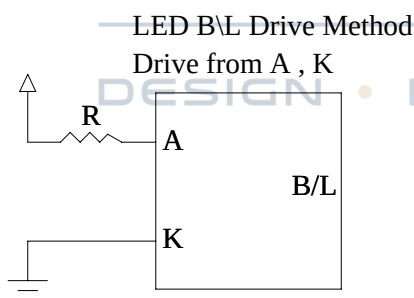
Backlight Information

Specification

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	I _{LED}	38.4	48	57.6	mA	V=3.5V
Supply Voltage	V	3.4	3.5	3.6	V	
Reverse Voltage	V _R	—	—	5	V	—
Luminous Intensity (Without LCD)	I _V	504	630	—	CD/M ²	I _{LED} =48mA
LED Life Time	—	—	50K	—	Hr.	I _{LED} ≤ 48mA
Color	Yellow Green					

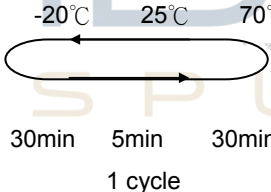
Note: The LED of B/L is drive by current only ; driving voltage is only for reference
To make driving current in safety area (waste current between minimum and maximum).

Note1 :50K hours is only an estimate for reference.



Reliability

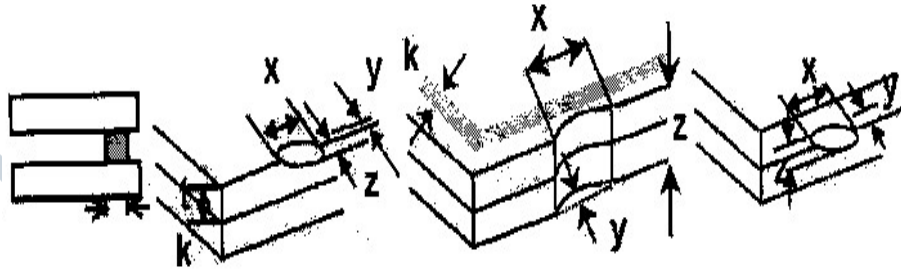
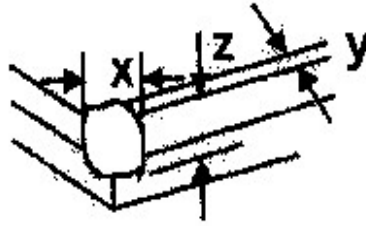
Content of Reliability Test (wide temperature, -20℃~70℃)

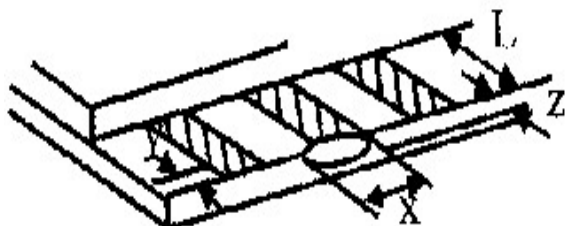
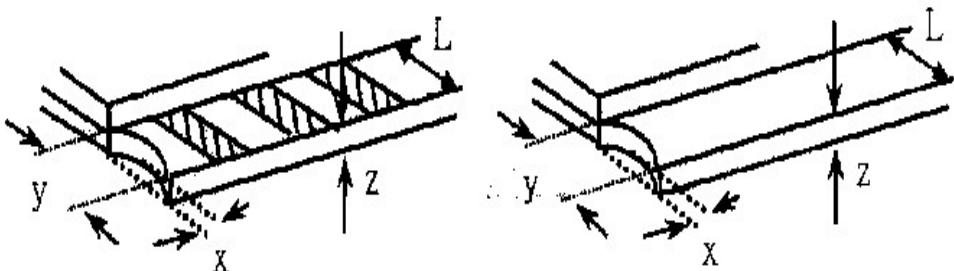
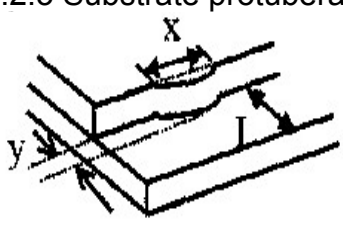
Environmental Test			
Test Item	Content of Test	Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80℃ 200hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30℃ 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70℃ 200hrs	-
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20℃ 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60℃,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60℃,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation  -20℃ 25℃ 70℃ 30min 5min 30min 1 cycle	-20℃/70℃ 10 cycles	-
Vibration test	Endurance test applying the vibration during transportation and using.	fixed amplitude: 15mm Vibration. Frequency: 10~55Hz. One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS= 1.5kΩ CS=100pF 1 time	—

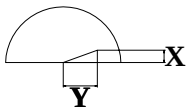
Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

NO	Item	Criterion	AQL									
05	Scratches	Follow NO.3 LCD black spots, white spots, contamination										
06	Chipped glass	Symbols Define: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length: 6.1 General glass chip : 6.1.1 Chip on panel surface and crack between panels: 	2.5									
		<table><tr><td>z: Chip thickness</td><td>y: Chip width</td><td>x: Chip length</td></tr><tr><td>$Z \leq 1/2t$</td><td>Not over viewing area</td><td>$x \leq 1/8a$</td></tr><tr><td>$1/2t < z \leq 2t$</td><td>Not exceed 1/3k</td><td>$x \leq 1/8a$</td></tr></table> ⊙ If there are 2 or more chips, x is total length of each chip. 6.1.2 Corner crack: 		z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$
		z: Chip thickness		y: Chip width	x: Chip length							
$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$										
$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$										
<table><tr><td>z: Chip thickness</td><td>y: Chip width</td><td>x: Chip length</td></tr><tr><td>$Z \leq 1/2t$</td><td>Not over viewing area</td><td>$x \leq 1/8a$</td></tr><tr><td>$1/2t < z \leq 2t$</td><td>Not exceed 1/3k</td><td>$x \leq 1/8a$</td></tr></table> ⊙ If there are 2 or more chips, x is the total length of each chip.	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$			
z: Chip thickness	y: Chip width	x: Chip length										
$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$										
$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$										

NO	Item	Criterion	AQL																
06	Glass crack	Symbols : x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length 6.2 Protrusion over terminal : 6.2.1 Chip on electrode pad :  <table><tr><td>y: Chip width</td><td>x: Chip length</td><td>z: Chip thickness</td></tr><tr><td>$y \leq 0.5\text{mm}$</td><td>$x \leq 1/8a$</td><td>$0 < z \leq t$</td></tr></table> 6.2.2 Non-conductive portion:  <table><tr><td>y: Chip width</td><td>x: Chip length</td><td>z: Chip thickness</td></tr><tr><td>$y \leq L$</td><td>$x \leq 1/8a$</td><td>$0 < z \leq t$</td></tr></table> ⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. ⊙ If the product will be heat sealed by the customer, the alignment mark not be damaged. 6.2.3 Substrate protuberance and internal crack.  <table><tr><td>y: width</td><td>x: length</td></tr><tr><td>$y \leq 1/3L$</td><td>$x \leq a$</td></tr></table>	y: Chip width	x: Chip length	z: Chip thickness	$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$	y: Chip width	x: Chip length	z: Chip thickness	$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$	y: width	x: length	$y \leq 1/3L$	$x \leq a$	2.5
		y: Chip width	x: Chip length	z: Chip thickness															
		$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$															
		y: Chip width	x: Chip length	z: Chip thickness															
$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$																	
y: width	x: length																		
$y \leq 1/3L$	$x \leq a$																		

NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong.	0.65 2.5 0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination. 9.2 Bezel must comply with job specifications.	2.5 0.65
10	PCB、COB	10.1 COB seal may not have pinholes larger than 0.2mm or contamination. 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10.9 The Scraping testing standard for Copper Coating of PCB  $X * Y \leq 2\text{mm}^2$	2.5 2.5 0.65 2.5 2.5 0.65 0.65 2.5 2.5
11	Soldering	11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB.	2.5 2.5 2.5 0.65

NO	Item	Criterion	AQL
12	General appearance	12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it causes the interface pin to sever.	2.5
		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened.	0.65
		12.8 Pin type must match type in specification sheet.	0.65
		12.9 LCD pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on packaging specification sheet.	0.65
		12.11 Product dimension and structure must conform to product specification sheet.	

MIDAS
DISPLAYS

Precautions in use of LCD Modules

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1. Avoid applying excessive shocks to the module or making any alterations or modifications to it.
2. Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
3. Don't disassemble the LCM.
4. Don't operate it above the absolute maximum rating.
5. Don't drop, bend or twist LCM.
6. Soldering: only to the I/O terminals.
7. Storage: please storage in anti-static electricity container and clean environment.
8. Midas have the right to change the passive components
(Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
9. Midas have the right to change the PCB Rev.



Material List of Components for RoHs

1. Midas Ltd. hereby declares that all of or part of products, including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A : The Harmful Material List

Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm
Above limited value is set up according to RoHS.						

2. Process for RoHS requirement :

(1) Use the Sn/Ag/Cu soldering surface; the surface of Pb-free solder is rougher than we used before.

(2) Heat-resistance temp. : _____

Reflow : 250°C, 30 seconds Max. ;

Connector soldering wave or hand soldering : 320°C, 10 seconds max.

(3) Temp. curve of reflow, max. Temp. : 235±5°C ;

Recommended customer's soldering temp. of connector : 280°C, 3 seconds.

Recommendable storage

1. Place the panel or module in the temperature 25°C±5°C and the humidity below 65% RH
2. Do not place the module near organics solvents or corrosive gases.
3. Do not crush, shake, or jolt the module